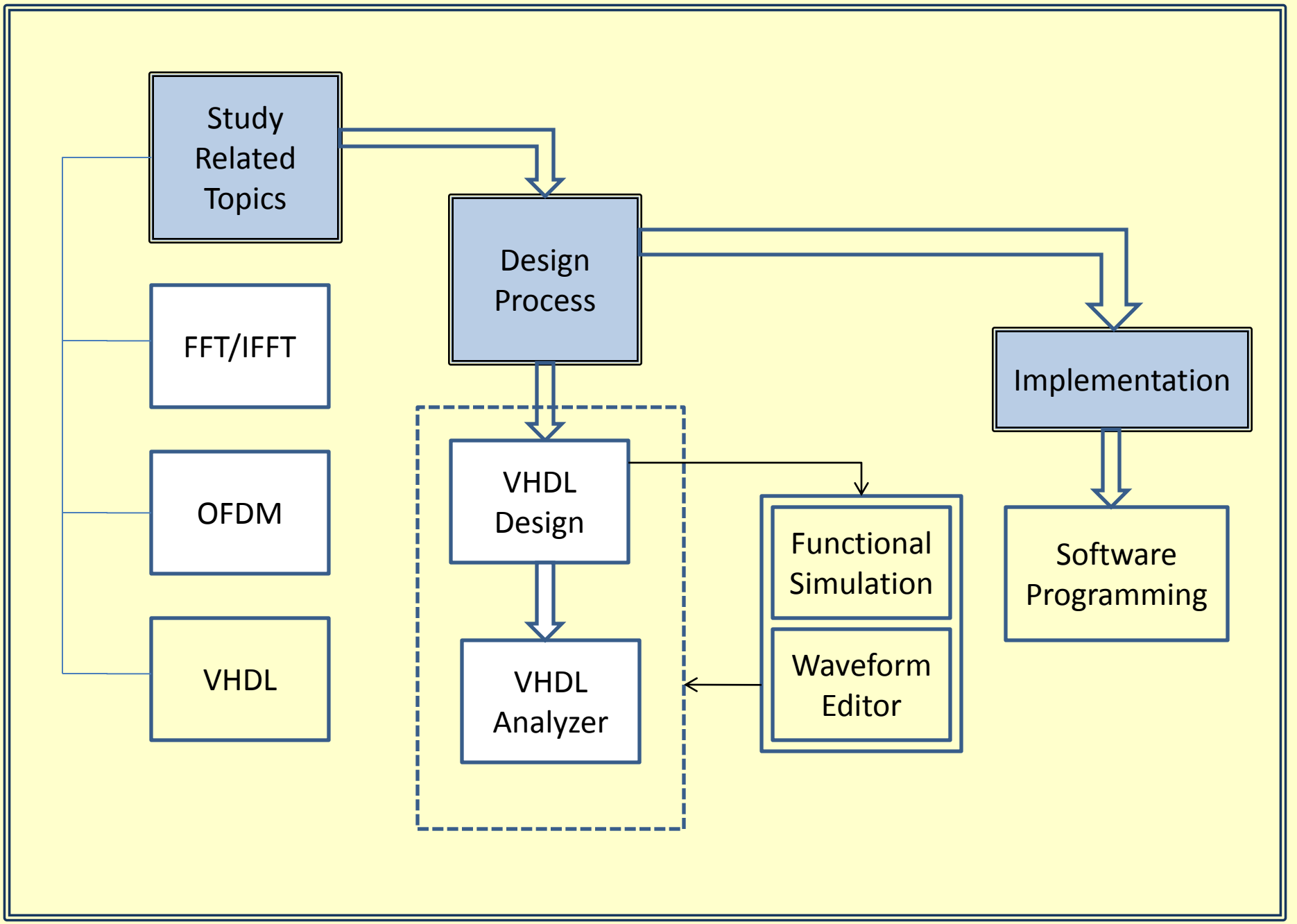
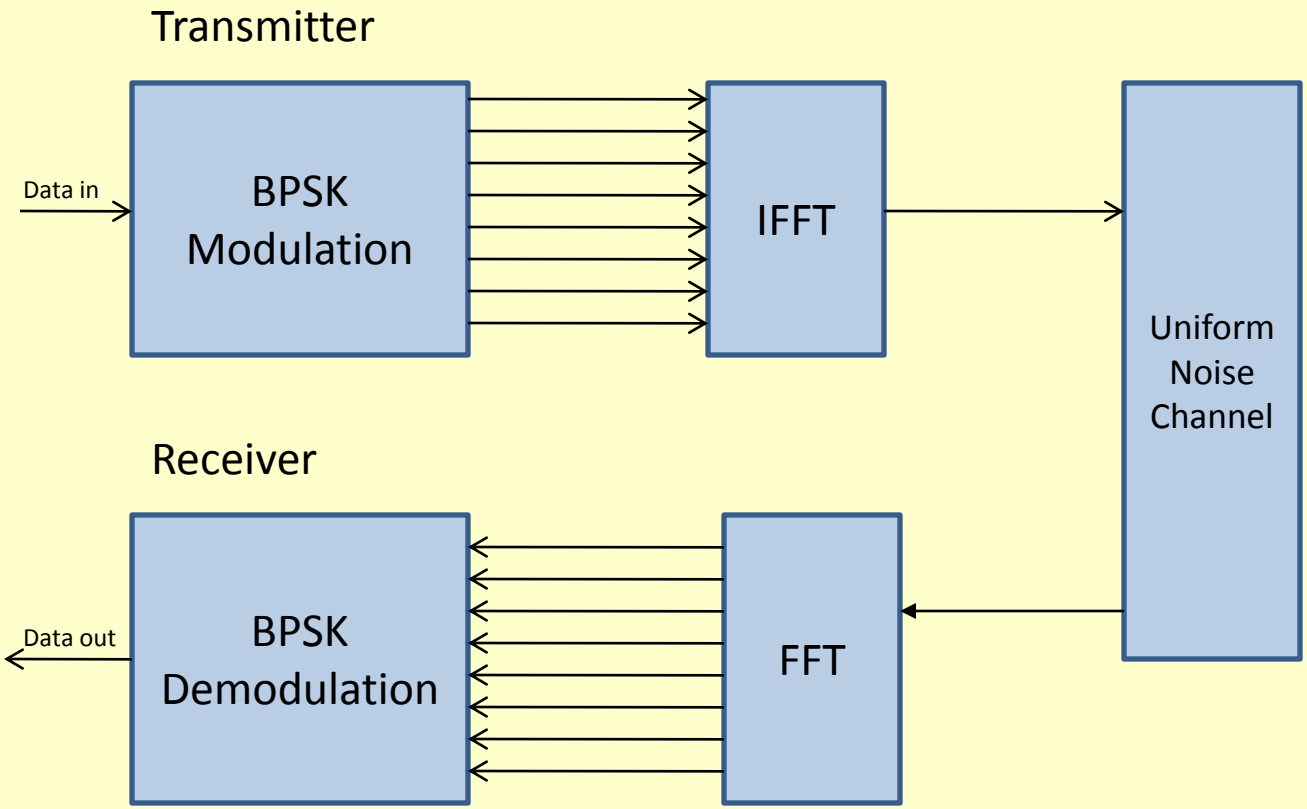
Design and Implementation of OFDM Transmitter and Receiver on FPGA hardware

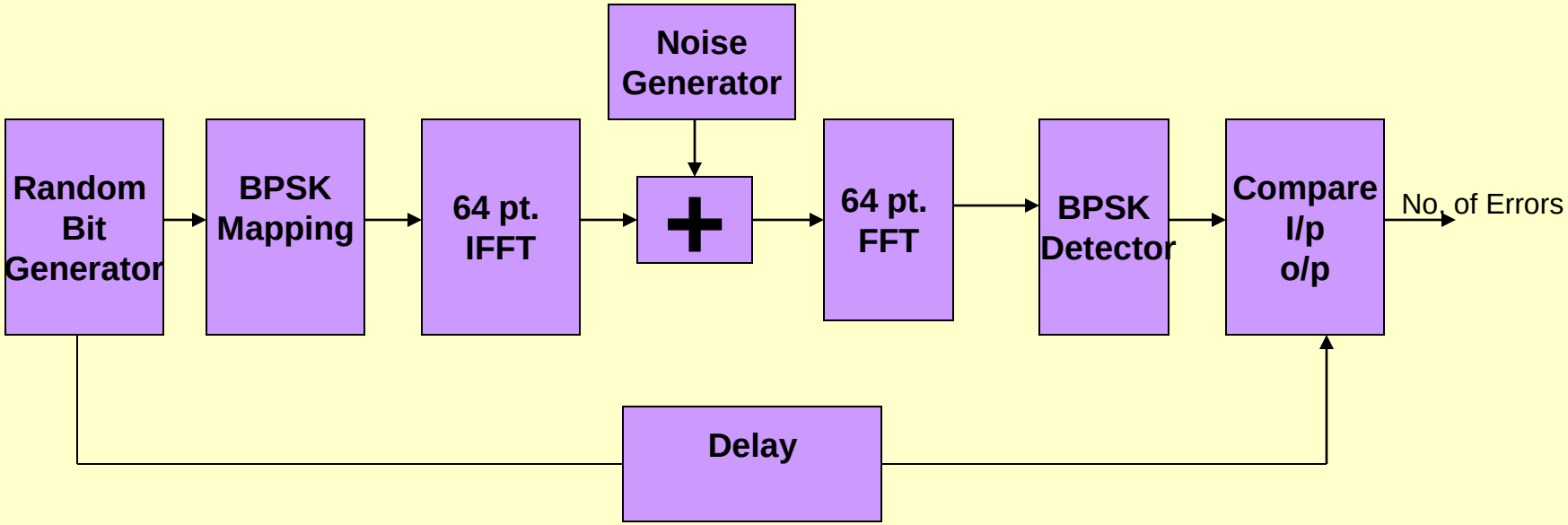
John Albanese
Zareen Khan


Study Related Topics
FFT/IFFT
OFDM
VHDL
Design Process
VHDL Design
VHDL Analyzer
Functional Simulation
Waveform Editor
Implementation
Software Programming

Block Diagram of OFDM Tx and RX



Flow Diagram



Observed Bit Error Rate

nPower(No. of bits shifted right)	10^4	10^5	10^6
0	4545	44764	447192
1	4201	42404	424980
2	3592	35494	355960
3	2602	25296	253990
4	501	4817	26226
5	31	310	1613
6	0	0	0
7	0	0	0
8	0	0	0
9	0	0	0
10	0	0	0
11	0	0	0
12	0	0	0
13	0	0	0
14	0	0	0
15	0	0	0